DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

My residence, post office address, and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter claimed and for which a patent is sought on the invention entitled:

DIFFUSION BARRIERS IN INTEGRATED CIRCUIT INTERCONNECTS

the specification of w is attached hereto		·			
		erial No and was amended	on (if a	pplicable).	
		d and understand the conters amended by any amendment			ified
America before this the same was not in prior to this applica	invention thereo public use or on tion. I acknowle	the same was ever known or use of the formore than one year prior asale in the United States of Adge the duty to disclose informaccordance with title 37, Control of the formatter of the same of the	to this appl America mo rmation wh	ication, and ore than one nich is know	that year yn to
or Section 365(b) of 365(a) of any PCT if the United States, I	f any foreign app international app isted below and	its under Title 35, United State plication(s) for patent or investigation which designated at have also identified belowing a filing date before that of	ntor's certification least one constant any foreign	ficate, or Se ountry other n applicatio	ction than n for
Prior Foreign App	lication(s):		~	.	
Number	Country	Day/Month/Year filed	Priority Yes	Claimed No	
I hereby claim the bapplication(s) listed		USC 119(e) of any United Sta	ites provisio	onal	
Prior Provisional	Application(s):	•			
Application Numb	er Filin	ng Date			

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s), or Section 365(c) of any PCT international application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT international application in the manner provided by the first paragraph of Title 35, United States Code, Section 112, I acknowledge the duty to disclose material information as defined in Title 37. Code of Federal Regulations, Section 1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

Prior U.S. Application(s):

Serial No. Filing Date Status; Patented, Pending, Abandoned

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

I hereby revoke any previous Powers of Attorney and appoint the following attorney(s) and/or agent(s), each said individual being a member or associate of The Law Offices of Mikio Ishimaru or being employed by Advanced Micro Devices, Inc.:

Mikio Ishimaru	Reg. No. 27,449	Paul S. Drake	Reg. No. 33,491
Elizabeth A. Apperley	Reg. No. 36,428	Rajendra Jaipershad	Reg. No. 44,168
Bradley Botsch	Reg. No. 34,552	Richard J. Roddy	Reg. No. 27,688
Michael Caywood	Reg. No. 37,797	Harry A. Wolin	Reg. No. 32,638
Daniel R. Collopy	Reg. No. 33,667	-	-

for so long as they remain with such law offices or company with full power of substitution and revocation, to prosecute this application and to transact in connection therewith all business in the Patent and Trademark Office and before competent International Authorities; said appointment to be to the exclusion of myself and my other attorney(s); and all future correspondence should be addressed to:

Mikio Ishimaru
The Law Offices of Mikio Ishimaru
1110 Sunnyvale – Saratoga Rd., Suite A1
Sunnyvale, California 94087

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Inventor's signature:	Sleval	_cur	uhnus	Date:	6/1	101
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Full name of sole or first inventor:

Steven C. Avenzino

Citizenship:

USA

Residence Address:

7504 Barnhart Place

Cupertino, CA 95014

P.O. Address:

Citizenship:

Same as Residence

Inventor's signature: Date: 5/31/0/

Full name of joint inventor: Pin-Chia Connie Wang

Republic of China

Residence Address:

461 Burgess Drive #2

Menlo Park, CA 94025

Post Office Address:

Same as Residence

Docket No. F0361

Inventor's signature: huh uy MDate: 5/31/01

Full name of joint inventor: Minh Van Ngo

Citizenship: <u>U.S.A.</u>

Residence Address: 40986 Canyon Heights Dr.

Fremont, CA 94539

P.O. Address: Same as Residence